A Fully-Programmable Neural Interface for Multi-Polar, Multi-Channel Stimulation Strategies

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Abstract—This paper describes a novel integrated electrode interface for multi-polar stimulation of multi-electrode arrays. This interface allows for simultaneous stimulation using multiple electrodes configured as source or sink with different phase and amplitudes in order to perform field shaping inside the tissue. The system is designed in an high voltage 0.18 µm CMOS process with 8 channels. It features an output voltage swing of 16V and current up to 0.5mA for electrode impedences of up to 30kΩ which is suitable for cuff and cortical grid arrays. This electrode interface comprises a digital module which stores stimulation settings and operates the different electrode channels. Here we present the full system architecture and simulation results.

I. INTRODUCTION

Neural prostheses is a growing field of study for the treatment of neurological disorders. Medical implants utilising these prostheses have been successfully developed to alleviate the symptoms of Parkinson’s disease, epilepsy, clinical depression and many others. These devices utilise electrical stimulation to modulate and/or suppress neuronal signalling by interfacing electronic devices to both the central and peripheral nervous system. For most of these devices, the stimulation settings are the results of clinical trials and specific adjustments for each patient [1]. So although effective, the mechanisms involved during electrical stimulation of neural tissue are not clearly understood.

Using multi-electrode arrays can provide better control of the stimulation. In particular, multi-polar configurations, in which the stimulation current is steered between several electrodes can shape the field in the tissue [2] and hence have better spatial selectivity. This type of selectivity is a key challenge in emerging medical devices, which allow unidirectional, fibre and fascicle-type selectivity and avoid unintentional excitation [3].

In addition, as the number of electrodes reaches the thousands, the ability to elicit more control and maintain device size and power consumption within acceptable safety limits for chronic use is essential. In the long term, factors such as electrode stability/degredation [4], neural fatigue and stimulation efficiency greatly affect the optimal performance of the neural prosthetic [5]. Hence, developing novel stimulation strategies is crucial both to improve prosthetic technology and to allow for implementation of stimulation strategies in medical devices.

There have been attempts to integrate efficient stimulation systems for multi-electrode arrays including [6], [7] and reference within. However, none have combined the programmability for different waveforms with versatile strategy for multi-polar stimulation strategies. When low power consumption and very large number of channels need to be achieved, designers usually adopt stimulation strategy involving only one active electrode at the time [8], [9]. Multi-polar stimulation interfaces have been designed on-chip [10] and on PCB [11]. While PCB solutions offer very large control on the stimulation, integrated designs feature much lower size with better matching and power consumption.

In this paper we present such a device, capable of multi-polar configurations and implementation of completely arbitrary stimulus waveforms. The paper is organized as followed: Section II presents the whole system, section III describes the main analogue circuits and the digital module, section IV presents simulation results of the full system and section V concludes the paper.

II. SYSTEM OVERVIEW

The architecture of the system consists of a PC-based GUI for defining stimulus parameters and hardware for generating stimulus waveforms (Fig. 1). This work focuses on the main hardware implementation that allows for active control of the electrodes and arbitrary waveform generation. Fig. 1b also shows an example multi-ring multi-electrode cuff where the rings can be used as multiple sources or sinks for field shaping and selective spatial stimulation (Fig. 1c).

The system developed (Fig. 2) contains circuitry to drive a single electrode and a main controller for global stimulus control. The individual channel circuitry, contains (1) Analogue circuits to produce voltage or current stimulus (up to 500µA for electrode impedences up to 30kΩ), remain floating
or act as a voltage reference and (2) Digital control circuitry, channel sequencer, for defining voltage or current stimulation, stimulus amplitude range and stimulus timings. The spatial and temporal resolution specifications were defined to be used with multi-electrode cuff electrodes [12].

The digital main controller and RAM use an SPI interface to store a custom waveform shape (which is common to all electrodes) and the individual channel specifications, which will be described in more detail later.

III. IMPLEMENTATION

In this section we will outline the main blocks implemented for the realisation of the analogue and digital circuitry with some of the applicable simulated results of each.

A. Front end circuit

The front end circuit is composed of a current DAC, a current conveyor, a transimpedance (current to voltage) and switches (Fig. 2). The DAC delivers a current in the range of $-100 \mu A$ to $100 \mu A$ which is a sub-range of the final output. This current is mirrored and amplified through a current conveyor which increases the voltage compliance to 20V. The DAC output can also be connected to a transimpedance in order to provide a voltage controlled output. The switches at the electrode end of the circuit are operated by the digital circuitry and define the electrode mode. The current conveyor and the transimpedence amplifier are supplied with 20V to obtain the necessary voltage headroom when stimulating tissue whereas the DAC is supplied with 1.8V in order to save power.

1) Current DAC: The DAC implemented in this system is based on a W-2W ladder. The W-2W ladder uses the same concept as R-2R ladder but with MOS transistors[13]. The main advantages of this type of DAC is that the ladder requires only two sizes of transistors and one bias. This feature ensures superior matching and limited use of die area which are particularly suitable for implementation of arrays [14].

In the proposed system, each channel can scale the pulse waveform. This scaling is achieved directly with the DAC structure chosen (Fig. 3). This DAC consists of two W-2W ladders: the p-channel ladder receives digital values of the waveform and the n-channel ladder scales the output. Consequently the final output current of the DAC is the product of two digital inputs $a_1a_2a_3a_4$ and $b_0b_1b_2$:

$$I_{out} = I_{ref} \left( \sum_{i=0}^{4} a_i \cdot 2^{i-4} \right) \cdot \left( \sum_{i=0}^{2} b_i \cdot 2^{i-2} \right)$$ (1)

The output of the DAC drives a current mirror which provides a sign selection: it can produce the input current as a current source or as a current sink. As mentioned the final output of the DAC can produce current from $\pm 100 \mu A$ and can be scaled depending on the necessary range, as shown in Fig. 4.

2) Current conveyor: In order to apply the stimulation current produced by the DAC to the electrode at the defined current levels (500\(\mu A\)) and with sufficient voltage headroom (16V) an output stage is required. This is achieved using a second generation current conveyor (Fig. 5). A class AB output stage is used to source both positive and negative currents. The current mirrors are cascoded to improve the output impedance. When used in the system, the voltage input (Y) is connected to ground, therefore potential of the current input (X) is also 0V. The output (Z) of the current conveyor has the opposite sign of the input (X).
3) Transimpedance: In order to apply a voltage to the electrode, the output current from the DAC needs to be converted into a voltage. A two-stage operational amplifier has been implemented to perform this transimpedance function (Fig. 5). The output voltage dynamic range can be set by the feedback resistor $R_f$ (2).

$$V_{out} = V_{ref} - R_f \cdot I_{in}$$ (2)

where the non-inverting input, $V_{ref}$ is connected to ground because no offset is needed in the output voltage.

B. Digital controller

The digital controller operates the analogue circuitry to produce electrical stimulations. Two entities have been designed to generate the necessary control signals: the channel sequencer and the main controller. As mentioned, the first one stores the settings related to one electrode and operates the analogue circuitry of one channel; the latter stores the pulse waveform, interpulse delay and transmits channel settings received from SPI to the individual channels.

The communication between the main controller and the channels is ensured by a single internal bus. Consequently, the main controller needs to specify the identifier of the channel before sending parameters on the bus. This communication structure simplifies the routing compared with a fully parallel communication bus between the main controller and the channels. It also eases the addition and deletion of channels as no change to the entities is required to handle less or more channels. The SPI streams structure which can be sent to the main controller are displayed in Fig. 6.

1) Principle of operation: A typical setting sequence includes the transmission of the pulse waveform and the interpulse delay followed by the loading of the settings for each channel of the system. Each operation starts with a command word and is followed by the corresponding configuration mode or to start or stop the stimulation.

When the waveform loading command is sent (wave cmd), the main controller copies the following SPI stream in the

RAM. Only the 6 LSBs of each SPI word contains waveform information. The MSB is used to notify if another waveform value will be sent in the next cycle or if the waveform has been entirely sent. When the channel settings command is sent (CH set cmd), the main controller first temporary stores the settings receive via SPI and then sends all these parameters via the internal bus to the channels. The channel identifier is used to activate the correct channel sequencer when settings are sent through the internal bus.

The system enters and exits stimulation mode via specific SPI commands. When an SPI start command is sent, the main controller transmits in turn a synchronous start command on the internal bus to activate all channel sequencers. Then it sends the pulse waveform on the internal bus followed by the interpulse delay and starts again until a stop command is received. When activated, channel sequencers enter the mode defined in their settings. Active channels delay incoming waveform in a buffer and apply the scale value to the DAC. They continue to output the pulse waveform until the number of repetitions is reached or until a stop command is asserted.

2) Stimulation settings: Table I lists the parameters available in the system. The pulse waveform is stored in the RAM. It consists on a list of DAC values representing the pulse shape. The interpulse delay, also stored in the RAM, is the time which separates two stimulation pulses. In this system the pulse frequency can be set from 2Hz to 1kHz.

The remaining settings are specific to every channel and are stored inside the channel sequencer. The mode of the channel defines how the channel operates during stimulation. Four active mode are available: two voltage modes (V) and two current modes (I). In order to perform multi-polar stimulation, the sign of the pulse waveform can be inverted ($\pm$). Three passive modes are available (Vdd, Vss and gnd). These modes can be used for unipolar stimulation or to shield some areas of the tissue during stimulation. The last electrode mode is

![Fig. 6. SPI stream for (a) waveform loading, (b) interpulse duration loading and (c) channel settings loading.](image)

<table>
<thead>
<tr>
<th>Setting (location)</th>
<th>Resolution</th>
<th>Range</th>
</tr>
</thead>
<tbody>
<tr>
<td>Pulse waveform (MC)</td>
<td>1,023×6-bit</td>
<td>0 to 1,023µs $\times \pm 500\mu$A/$\pm 8V$</td>
</tr>
<tr>
<td>Interpulse delay (MC)</td>
<td>8-bit</td>
<td>1,023 to 522µs (2.048µs steps)</td>
</tr>
<tr>
<td>Mode (CS)</td>
<td>3-bit</td>
<td>±V, ±I, HighZ, Vdd/Vss/Und</td>
</tr>
<tr>
<td>Scale (CS)</td>
<td>1-bit</td>
<td>1/7 to 7/7</td>
</tr>
<tr>
<td>Delay duration (CS)</td>
<td>9-bit</td>
<td>0 to 511µs</td>
</tr>
<tr>
<td>Repetition number (CS)</td>
<td>12-bit</td>
<td>0 to 4,095 times</td>
</tr>
</tbody>
</table>

MC: Main Controller  
CS: Channel Sequencer
In this work we have shown an 8-channel system which can interface to electrodes arrays. Both the analogue design and the digital circuit have been covered. Increasing the number of channels has been made particularly simple due to the unique bus between the main controller and the channels. Moreover, large matching has been achieved allowing accurate distribution of currents between channels.

Specific settings are available for each channel allowing deeper control of stimulations by clinicians. In particular the selection of different scales and delays between the channels allows experimenting electrical field shaping inducing more acute response of the tissue. The choice of the pulse waveform is a parameter which can be used to provide safer and more efficient stimulations strategy.

The next iteration of this work will involve fabricating this device then testing it’s field shaping capabilities compared to modelling theory.

V. CONCLUSION

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REFERENCES


